

Amendments to the Claims:

## Claims 1-9 (Cancelled)

10. (Currently amended) A data communication apparatus, comprising:
- a controller arranged to establish communication in a first mode with another data communication apparatus, the controller arranged to switch to communication in a second mode with said another data communication apparatus after communication is established;
  - an input for receiving original data bits that are to be transmitted via a communication channel to said another data communication apparatus;
  - an encoder coupled to said input for applying to the original data bits an encoding algorithm that produces parity bits;
  - an output for providing bits that are to be transmitted across the communication channel;
  - and
  - a data path coupled between said encoder and said output, said data path receiving information from said another data communication apparatus, said data path selecting one of the original data bits with Cyclical Redundancy Check (CRC) bits and the parity bits in response to a ~~first information~~ an acknowledgement, said data path selecting the other of the original data bits with CRC bits and the parity bits in response to a ~~second information~~ a negative acknowledgement indicating that an earlier transmission has not been received correctly at said another communication apparatus. said data path responsive to the negative acknowledgement for changing its selection from one of the original data bits with CRC bits and the parity bits to the other of the original data bits with CRC bits and the parity bits, to be provided to said output for transmission across the communication channel to said another data communication apparatus.
11. (Previously amended) The apparatus of Claim 10, wherein said data path includes a buffer coupled to said encoder for storing the original data bits and the parity bits.

12. (Previously amended) The apparatus of Claim 11, wherein said data path includes a selector coupled between said buffer and said output, said selector responsive to said information for obtaining one of the original data bits with CRC bits and the parity bits from said buffer to be provided to said output for transmission to said another data communication apparatus.
13. (Cancelled)
14. (Original) The apparatus of Claim 10, provided as a wireless communication apparatus.
15. (Original) The apparatus of Claim 10, wherein said encoder is a convolutional encoder.
16. (Previously amended) A data communication apparatus, comprising:  
a controller arranged to establish communication in a first mode with another data communication apparatus, the controller arranged to switch to communication in a second mode with said another data communication apparatus after communication is established;  
an input for receiving a received version of original bits with Cyclical Redundancy Check (CRC) bits in response to a first information without parity bits produced at said another data communication apparatus by operation of an encoding algorithm applied to the original bits, said input receiving said parity bits in response to a second information, said original bits with CRC bits and parity bits transmitted over a communication channel by said another data communication apparatus;  
an error detector coupled to said input for determining whether the received version of the original data bits is correct in response to the CRC bits; and  
a controller coupled to said error detector, responsive to a determination that the received version of the original data bits is correct for providing said first information to said another data communication apparatus, and responsive to a determination that the received version of the original data bits is incorrect for providing said second information to said another data communication apparatus.

17. (Previously amended) The apparatus of Claim 16, wherein said input is further for receiving a received version of the parity bits as transmitted from said another data communication apparatus, said controller coupled to said input for applying to the received version of the parity bits a mapping operation which, if the parity bits have been received correctly at the receiving end, will result in the original data bits, said error detector coupled to said controller for applying an error detection procedure to the result of the mapping operation to determine whether the mapping operation has resulted in the original data bits.

18. (Previously amended) The apparatus of Claim 17, including a decoder coupled to said input and said controller, said controller responsive to a determination by said error detector that the mapping operation has not resulted in the original data bits for signaling said decoder to apply to the received version of the original data bits and the received version of the parity bits a decoding algorithm that corresponds to said encoding algorithm.

19. (Previously amended) The apparatus of Claim 18, including a buffer coupled between said input and said decoder for storing the received version of the original bits and the received version of the parity bits for use by said decoder.

20. (Previously amended) The apparatus of Claim 18, wherein said error detector is coupled to said decoder for determining whether said decoding algorithm has resulted in the original data bits, said controller operable in response to a determination that said decoding algorithm has not resulted in the original data bits for providing for transmission to said another data communication apparatus a request for retransmission of the original data bits with CRC bits.

21. (Original) The apparatus of Claim 18, wherein said decoder is a Viterbi decoder.

22. (Original) The apparatus of Claim 16, provided as a wireless communication apparatus.

Claims 23-32 (Cancelled)

33. (Currently amended) A method of communicating data from a transmitting end to a receiving end, comprising:

establishing communication in a first mode with the receiving end;

switching to a second mode of communication with the receiving end after communication is established;

the transmitting end applying to a plurality of original data bits that are to be transmitted to the receiving end an encoding algorithm that produces Cyclical Redundancy Check (CRC) bits and parity bits;

the transmitting end transmitting the original data bits and the CRC bits without the parity bits in a first transmission to the receiving end; and

the transmitting end refraining from transmitting the parity bits until the transmitting end receives an indication from the receiving end that the original data bits have not been correctly received at the receiving end;

the transmitting end transmitting the parity bits to the receiving end in a second transmission in response to an indication from the receiving end that the original data bits have not been correctly received at the receiving end; and

the receiving end combining a received version of the original data bits and a received version of the parity bits to produce a combined set of received bits, and the receiving end applying to the combined set of received bits a decoding algorithm that corresponds to said encoding algorithm.

34. (Cancelled)

35. (Cancelled)

36. (Currently amended) The method of Claim 34 ~~33~~, including the receiving end applying to a received version of the parity bits a mapping operation which, if the parity bits have been received correctly at the receiving end, will result in the original data bits, and the receiving end applying an error detection procedure to the result of the mapping operation to determine whether the mapping operation has resulted in the original data bits and, in response to a determination

that the mapping operation has not resulted in the original data bits, the receiving end combining the received version of the parity bits with a received version of the original data bits to produce a combined set of received bits, and the receiving end applying to the combined set of received bits a decoding algorithm that corresponds to said encoding algorithm.

37. (Previously added) The method of Claim 36, wherein said encoding and decoding algorithms are Viterbi encoding and decoding algorithms.

38. (Previously added) The method of Claim 36, including the receiving end applying an error detection procedure to a result of said decoding algorithm with the CRC bits to determine whether said decoding algorithm has resulted in the original data bits and, in response to a determination that said decoding algorithm has not resulted in the original data bits, the receiving end transmitting to the transmitting end a request for retransmission of the original data bits.

39. (Previously added) The method of Claim 38, including the transmitting end retransmitting the original data bits to the receiving end and, in response to a determination by the receiving end that said retransmission of the original data bits has not been received correctly, the receiving end combining a received version of the retransmitted original data bits with said received version of the parity bits to produce another combined set of received bits, and the receiving end applying said decoding algorithm to said another combined set of received bits.

40. (Previously amended) A method of communicating data from a transmitting end to a receiving end, comprising:

establishing communication in a first mode with the transmitting end;

switching to a second mode of communication with the transmitting end after communication is established;

the receiving end receiving from the transmitting end a first transmission including original data bits and Cyclical Redundancy Check (CRC) bits without parity bits produced at the transmitting end by operation of an encoding algorithm applied to the original data bits;

the receiving end determining whether the original data bits have been received correctly in response to the CRC bits and, responsive to a determination that the original data bits have not been received correctly, the receiving end transmitting to the transmitting end a request for transmission of the parity bits; and

the receiving end combining a received version of the original data bits and a received version of the parity bits to produce a combined set of received bits, and the receiving end applying to the combined set of received bits a decoding algorithm that corresponds to said encoding algorithm.

41. (Previously added) The method of Claim 40, wherein the encoding algorithm is a convolutional encoding algorithm.

42. (Currently amended) A method of transmitting data, comprising:

encoding data by a first method to establish communication with a remote receiver;

encoding data by a second method to communicate with the remote receiver after communication is established;

applying an encoding algorithm that produces parity bits to a plurality of original data bits that are to be transmitted;

transmitting the original data bits with Cyclical Redundancy Check (CRC) bits to the remote receiver without the parity bits in a first transmission; and

refraining from transmitting the parity bits until receiving an indication that the original data bits have not been correctly received;

transmitting the parity bits in a second transmission in response to the indication that the original data bits have not been correctly received; and

the receiving end combining a received version of the original data bits and a received version of the parity bits to produce a combined set of received bits, and the receiving end applying to the combined set of received bits a decoding algorithm that corresponds to said encoding algorithm.

43. (Cancelled)

44. (Previously added) The method of Claim 42, wherein said encoding algorithm is a Viterbi encoding algorithm.

45. (Previously added) The method of Claim 42, comprising retransmitting the original data bits.

46. (Previously amended) A method of receiving data, comprising:

encoding data by a first method to establish communication with a remote transmitter;

encoding data by a second method to communicate with the remote transmitter after communication is established;

receiving a first transmission from the remote transmitter including original data bits and Cyclical Redundancy Check (CRC) bits without parity bits produced by operation of an encoding algorithm applied to the original data bits;

determining that the original data bits have not been received correctly in response to the CRC bits; and

transmitting a request for transmission of parity bits to the remote transmitter responsive to the step of determining;

combining a received version of the original data bits and a received version of the parity bits to produce a combined set of received bits; and

applying a decoding algorithm that corresponds to said encoding algorithm to the combined set of received bits to produce decoded data bits.

47. (Previously added) The method of Claim 46, wherein the encoding algorithm is a convolutional encoding algorithm.

48. (Cancelled)

49. (Currently amended) The method of Claim ~~48~~ 46, comprising:  
applying a CRC error detection procedure to the decoded data bits;  
determining the decoded data bits are not the same as the original data bits in response to the step of applying; and  
transmitting a request for retransmission of the original data bits in response to the step of determining.
50. (Currently amended) The method of Claim ~~48~~ 46, comprising:  
receiving a retransmission of the original data bits and a retransmission of the CRC bits;  
determining that said retransmission of the original data bits has not been received correctly in response to the retransmission of the CRC bits;  
combining a received version of the retransmitted original data bits with said received version of the parity bits to produce another combined set of received bits; and  
applying said decoding algorithm to said another combined set of received bits.
51. (Previously added) The method of Claim 46, comprising:  
applying a mapping operation to a received version of the parity bits to produce resultant data bits;  
applying a CRC error detection procedure to the resultant data bits;  
determining that the resultant data bits are not the same as the original data bits in response to the step of applying a CRC error detection procedure;  
combining the received version of the parity bits with a received version of the original data bits to produce a combined set of received bits; and  
applying a decoding algorithm that corresponds to said encoding algorithm to the combined set of received bits.